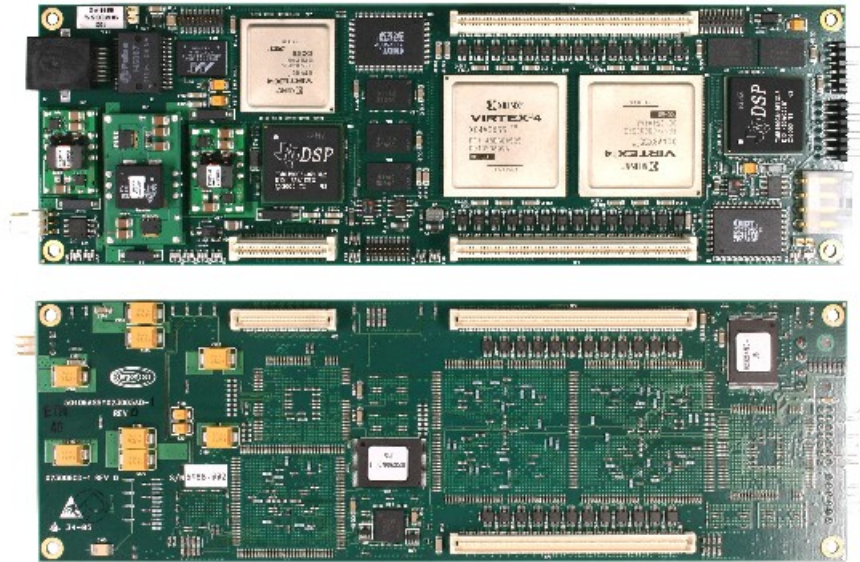


GEN-5

Digital Receiver Card

G-5 Receiver Board Layout



Features

- Input Channels: Six, 12-bit, 200MSPs A/D channels
- Signal Coverage: 504 MHz total bandwidth
- Channel Selection:
 - 24 – 48 Independent Digital Quadrature Tuners (DQTs)
 - Tuning Resolution: sub-Hertz selectivity via software commands
- Dual XILINIX Virtex-4 SX55 FPGAs
- Dual Texas Instruments TMS320C6416T 1GHz DSPs
- 300 MHz PowerPC 405 Control Processor hosted on a XILINIX Virtex-4 FX60 FPGA
- Demodulation/Decoding: Software and/or hardware implementation, many formats supported
- Channels: 24 – 48 channels per card
- Interfaces:
 - 1 – 115,200 baud RS-232 serial port
 - 1 – 10/100/1000 Mbps Tri-Mode Ethernet

For further information, please contact:
ARGON ST, Inc.
8419 Terminal Road
Newington, VA 22122
PH: (703)550-7000 FAX: (703)541-1554

